

SHORT CURRICULUM VITAE

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Education

Ph.D. in Computer Engineering, Sapienza University of Rome, Italy (March 2012).

Thesis Title: "Performance Models of Concurrency Control Protocols for Transaction Processing Systems".

M.S. Degree in Computer Engineering, Sapienza University of Rome, Italy (February 2008). Final rank: 110/110 cum laude. Thesis deemed worthy of publication.

Research Interests

My research interests lie in the area of computing architectures and distributed systems, including:

Performance Modeling, Evaluation, optimization

- Analytical, simulation and machine learning-based performance modeling, workload characterization, performance analysis and evaluation

Performance Optimization and Self-tuning

- Workload/QoS-driven performance optimization
- Elastic-scaling and self-tuning data replication
- Self-optimizing concurrency mechanisms
- Adaptive workload monitoring and characterization

Concurrent and Parallel Programming:

- Synchronization mechanisms in concurrent applications
- Transactional Memories
- High performance simulation platforms

Cloud Computing:

- Cloud infrastructure and application performance analysis
- Auto-scaling/self-configuring cloud platforms
- Automated workload characterization

Energy-efficient computing:

- Energy efficiency models
- Energy optimization mechanisms in concurrent and distributed applications

Publications:

The list of my publications is available on my web site: <http://www.dis.uniroma1.it/~disanzo/>

Research positions

- September 2016 – Today
Research Assistant at DIAG - Department of Computer, Control, and Management Engineering, Sapienza University of Rome, Italy.
- June 2014 – May 2016
Researcher on contract at IRIANC - International Research Institute for Autonomic Network Computing - Boston, US / Munich, Germany.
- August 2013 – February 2015
Researcher on contract at CINFAI - Consorzio Nazionale Interuniversitario Per la Fisica delle Atmosfere e delle Idrosfere
- May 2013 – June 2013
Visiting researcher at INESC-ID (Instituto de Engenharia de Sistemas e Computadores Investigação e Desenvolvimento), Lisboa, Portugal
- January 2011 – April 2013
Researcher on contract at CINI - Consorzio Interuniversitario Nazionale per l'Informatica, Rome, Italy.
- March 2009 – February 2011
Research Assistant at Department of Computer, Control, and Management Engineering, Faculty of Engineering, Sapienza University of Rome, Italy.
- June 2008 – April 2009
Research on contract at CINI - Consorzio Interuniversitario Nazionale per l'Informatica, Rome, Italy.

Academic Teaching

Professor on contract:

- **Database Systems**, B.S. Degree in Computer Engineering, Sapienza University of Rome, Rieti. (2009-2011).

Lecturer:

- **Concurrent and Parallel Programming. Module: Transactional Memories**. MS Degree in Computer Engineering, Sapienza University of Rome (since 2013).

Teaching Assistant:

- **Capacity Planning**. MS Degree in Computer Engineering, Sapienza University of Rome (Since 2012)
- **Foundations of Computer Science**. BS Degree in Computer Engineering, Sapienza University of Rome, Rieti (2008-2010).
- **Operating Systems**. BS Degree in Computer Engineering, Sapienza University of Rome, Rieti (2008-2009).