

# Verification and Synthesis of Reactive Programs

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## Lectures Outline

- Overview of System Synthesis.
- Fair Discrete Systems and their Computations.
- Model Checking **Invariance** and **response**.
- **Temporal Testers** and general **LTL** Model Checking.
- **Controller Synthesis** via **Games**.
- Synthesis from **Recurrence** Specifications.
- Synthesis from **Reactivity** Specifications. – The general case.

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## Motivation

Why **verify**, if we can automatically synthesize a program which is **correct by construction**?

## Applying Mathematics to the Programming Problem

The mathematical paradigm considers a **constraint**  $C(x)$ , e.g.

$$2 < x \leq 10$$

and asks questions such as:

1. Does  $x = 5$  satisfy the constraint?
2. Is the constraint satisfiable by some  $x$ ?
3. Find an  $x$  which satisfies the constraint.
4. Find the **best**, say **maximal**,  $x$  which satisfies  $C$ .

**Question:** If  $x$  is the program, what is  $C$ ?

**Answer:**  $C$  is the specification which the program should satisfy.

**Program Verification** solves **Problem no. 1**.

**Program Synthesis** solves **Problems no. 2 and 3**.

Why perform a **post-facto Verification** if you can **synthesize a constructively Correct program directly from the specification**?

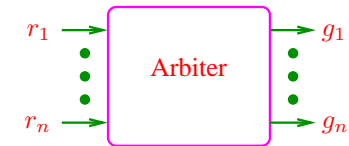
## A Brief History of System Synthesis

In 1965 Church formulated the following **Church problem**: Given a circuit interface specification (identification of **input** and **output** variables) and a behavioral specification,

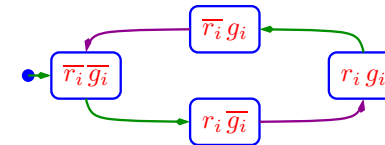
- Determine if there exists an automaton (sequential circuit) which realizes the specification.
- If the specification is **realizable**, construct an implementing circuit

The specification was given in the **sequence calculus** which is an explicit-time temporal logic.

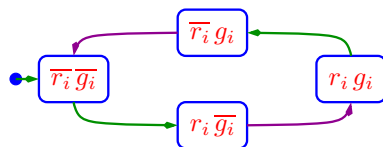
## Example of a Specification: Arbiter



The protocol for each client:



## The Behavioral Specification



$$\bigwedge_i \forall t : (r_i[t] = g_i[t] \rightarrow g_i[t+1] = g_i[t]) \wedge (r_i[t] \neq g_i[t] \rightarrow r_i[t+1] = r_i[t]) \quad \wedge$$

$$\bigwedge_{i \neq j} \forall t : \neg(g_i[t] \wedge g_j[t]) \quad \wedge$$

$$\bigwedge_i \forall t : r_i[t] \neq g_i[t] \rightarrow \exists s \geq t : r_i[s] = g_i[s]$$

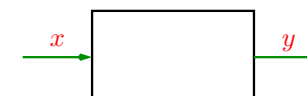
Is this specification **realizable**?

The essence of synthesis is the conversion

**From relations to Functions.**

## From Relations to Functions

Consider a computational program:



- The relation  $x = y^2$  is a specification for the program computing the function  $y = \sqrt{x}$ .
- The relation  $x \models y$  is a specification for the program that finds a satisfying assignment to the **CNF** boolean formula  $x$ .

**Checking** is easier than **computing**.

## Solutions to Church's Problem

In 1969, M. Rabin provided a first solution to Church's problem. Solution was based on automata on Infinite Trees. All the concepts involving  $\omega$ -automata were invented for this work.

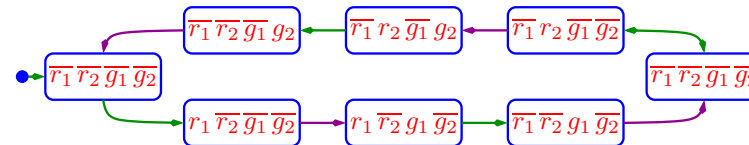
At the same year, Büchi and Landweber provided another solution, based on infinite games.

These two techniques (Trees and Games) are still the main techniques for performing synthesis.

## Synthesis of Reactive Modules from Temporal Specifications

Around 1981 Wolper and Emerson, each in his preferred brand of temporal logic (linear and branching, respectively), considered the problem of synthesis of reactive systems from temporal specifications.

Their (common) conclusion was that specification  $\varphi$  is realizable iff it is satisfiable, and that an implementing program can be extracted from a satisfying model in the tableau. A typical solution they would obtain for the arbiter problem is:



Such solutions are acceptable only in circumstances when the environment fully cooperate with the system.

## Next Step: Realizability $\square$ Satisfiability

There are two different reasons why a specification may fail to be realizable.

### Inconsistency

$$\diamond g \wedge \square \neg g$$

**Unrealizability** For a system



Realizing the specification

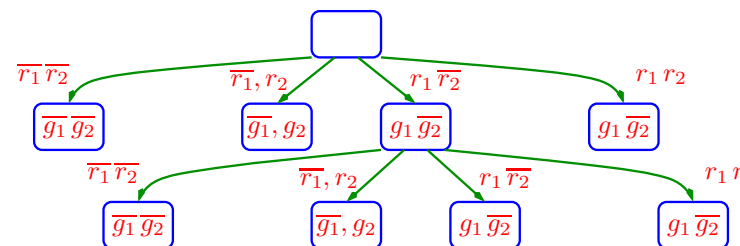
$$g \leftrightarrow \diamond r$$

requires clairvoyance.

## A Synthesized Module Should Maintain Specification Against Adversarial Environment

In 1998, Rosner claimed that realizability should guarantee the specification against all possible (including adversarial) environments.

To solve the problem one must find a satisfying tree where the branching represents all possible inputs:



Can be formulated as satisfaction of the CTL\* formula

$$A\varphi \wedge A\square (\text{EX}(\overline{r_1} \wedge \overline{r_2}) \wedge \text{EX}(\overline{r_1} \wedge r_2) \wedge \text{EX}(r_1 \wedge \overline{r_2}) \wedge \text{EX}(r_1 \wedge r_2))$$

## Bad Complexity

Rosner and P have shown [1989] that the synthesis process has worst case complexity which is **doubly exponential**. The first exponent comes from the translation of  $\varphi$  into a non-deterministic Büchautomaton. The second exponent is due to the determinization of the automaton.

This result doomed synthesis to be considered highly untractable.

## Simple Cases of Lower Complexity

In 1989, Ramadge and Wonham introduced the notion of **controller synthesis** and showed that for a specification of the form  $\square p$ , the controller can be synthesized in linear time.

In 1998, Asarin, Maler, P, and Sifakis, extended controller synthesis to timed systems, and showed that for specifications of the form  $\square p$  and  $\diamond q$ , the problem can be solved by symbolic methods in linear time.

## Lessons to be Learned from these Lectures

- Program (and design) synthesis is a tractable process.
- It can be solved using symbolic methods based on fixed-point iterations in a way very similar to model checking.
- The complexity of the solution is always polynomial where, unlike model checking, the degree of the polynomial depends on the structural complexity of the specification  $\varphi$ .
- For a very large class of specifications, arising in practice, the degree is 3, i.e., the problem can be solved in time  $n^3$ .

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## Examples of Computations

Identification of the FDS  $\mathcal{D}_P$  corresponding to a program  $P$  gives rise to a set of computations  $Comp(P) = Comp(\mathcal{D}_P)$ .

The following computation of program ANY-Y corresponds to the case that  $m_0$  is the first executed statement:

$$\begin{aligned} \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 0 \rangle &\xrightarrow{m_0} \langle \pi_1: \ell_0, \pi_2: m_1; x: 1, y: 0 \rangle \xrightarrow{\ell_0} \\ \langle \pi_1: \ell_2, \pi_2: m_1; x: 1, y: 0 \rangle &\xrightarrow{\tau_I} \dots \xrightarrow{\tau_I} \dots \end{aligned}$$

The following computation corresponds to the case that statement  $\ell_1$  is executed before  $m_0$ .

$$\begin{aligned} \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 0 \rangle &\xrightarrow{\ell_0} \langle \pi_1: \ell_1, \pi_2: m_0; x: 0, y: 0 \rangle \xrightarrow{\ell_1} \\ \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 1 \rangle &\xrightarrow{m_0} \langle \pi_1: \ell_0, \pi_2: m_1; x: 1, y: 1 \rangle \xrightarrow{\ell_0} \\ \langle \pi_1: \ell_2, \pi_2: m_1; x: 1, y: 1 \rangle &\xrightarrow{\tau_I} \dots \xrightarrow{\tau_I} \dots \end{aligned}$$

In a similar way, we can construct for each  $n \geq 0$  a computation that executes the body of statement  $\ell_0$   $n$  times and then terminates in the final state

$$\langle \pi_1: \ell_2, \pi_2: m_1; x: 1, y: n \rangle.$$

## A Non-Computation

While we can delay termination of the program for an arbitrary long time, we cannot postpone it forever.

Thus, the sequence

$$\begin{aligned} \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 0 \rangle &\xrightarrow{\ell_0} \langle \pi_1: \ell_1, \pi_2: m_0; x: 0, y: 0 \rangle \xrightarrow{\ell_1} \\ \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 1 \rangle &\xrightarrow{\ell_0} \langle \pi_1: \ell_1, \pi_2: m_0; x: 0, y: 1 \rangle \xrightarrow{\ell_1} \\ \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 2 \rangle &\xrightarrow{\ell_0} \langle \pi_1: \ell_1, \pi_2: m_0; x: 0, y: 2 \rangle \xrightarrow{\ell_1} \\ \langle \pi_1: \ell_0, \pi_2: m_0; x: 0, y: 3 \rangle &\xrightarrow{\ell_0} \dots \end{aligned}$$

in which statement  $m_0$  is never executed is not an admissible computation. This is because it violates the justice requirement  $\text{-at-}m_0$  contributed by statement  $m_0$ , by having no states in which this requirement holds.

This illustrates how the requirement of justice ensures that program ANY-Y always terminates.

Justice guarantees that every (enabled) process eventually progresses, in spite of the representation of concurrency by interleaving.

## Justice is not Enough. You also Need Compassion

The following program MUX-SEM, implements mutual exclusion by semaphores.

$y$ : natural initially  $y = 1$

$$P_1 :: \left[ \begin{array}{l} \ell_0 : \text{loop forever do} \\ \quad \left[ \begin{array}{l} \ell_1 : \text{Non-critical} \\ \ell_2 : \text{request } y \\ \ell_3 : \text{Critical} \\ \ell_4 : \text{release } y \end{array} \right] \end{array} \right] \parallel P_2 :: \left[ \begin{array}{l} m_0 : \text{loop forever do} \\ \quad \left[ \begin{array}{l} m_1 : \text{Non-critical} \\ m_2 : \text{request } y \\ m_3 : \text{Critical} \\ m_4 : \text{release } y \end{array} \right] \end{array} \right]$$

The compassion set of this program consists of

$$\mathcal{C}: \{(\text{at-}\ell_2 \wedge y > 0, \text{at-}\ell_3), (\text{at-}m_2 \wedge y > 0, \text{at-}m_3)\}.$$

Usually, with a compassionate transition  $\tau$ , we associate the compassion requirement

$$(En(\tau), \text{taken}(\tau))$$

## Program MUX-SEM

should satisfy the following two requirements:

- **Mutual Exclusion** – No computation of the program can include a state in which process  $P_1$  is at  $\ell_3$  while  $P_2$  is at  $m_3$ .
- **Accessibility** – Whenever process  $P_1$  is at  $\ell_2$ , it shall eventually reach its critical section at  $\ell_3$ . Similar requirement for  $P_2$ .

Consider the state sequence:

$$\begin{aligned} \sigma: \langle \ell_0, m_0, 1 \rangle &\longrightarrow \dots \longrightarrow \langle \ell_2, m_2, 1 \rangle \xrightarrow{m_2} \\ \langle \ell_2, m_3, 0 \rangle &\xrightarrow{m_3} \langle \ell_2, m_4, 0 \rangle \xrightarrow{m_4} \\ \langle \ell_2, m_0, 1 \rangle &\xrightarrow{m_0} \langle \ell_2, m_1, 1 \rangle \xrightarrow{m_1} \langle \ell_2, m_2, 1 \rangle \xrightarrow{m_2} \\ \langle \ell_2, m_3, 0 \rangle &\longrightarrow \dots, \end{aligned}$$

which violates accessibility for process  $P_1$ . Due to the requirement of compassion for  $\ell_2$ , it is not a computation, and accessibility is guaranteed.

**Conclusion:** Justice alone is not sufficient !!!

## FDS Operations: Asynchronous Parallel Composition

The **asynchronous parallel composition** of systems  $\mathcal{D}_1$  and  $\mathcal{D}_2$ , denoted by  $\mathcal{D}_1 \parallel \mathcal{D}_2$ , is given by  $\mathcal{D} = \langle V, \Theta, \rho, \mathcal{J}, \mathcal{C} \rangle$ , where

$$\begin{aligned} V &= V_1 \cup V_2 \\ \Theta &= \Theta_1 \wedge \Theta_2 \\ \rho &= (\rho_1 \wedge \text{pres}(V_2 - V_1)) \vee (\rho_2 \wedge \text{pres}(V_1 - V_2)) \\ \mathcal{J} &= \mathcal{J}_1 \cup \mathcal{J}_2 \\ \mathcal{C} &= \mathcal{C}_1 \cup \mathcal{C}_2 \end{aligned}$$

The predicate  $\text{pres}(U)$  stands for the assertion  $U' = U$ , implying that all the variables in  $U$  are preserved by the transition.

**Asynchronous parallel composition** represents the **interleaving**-based concurrency which is assumed in **shared-variables** models.

**Claim 1.**  $\mathcal{D}(P_1 \parallel P_2) \sim \mathcal{D}(P_1) \parallel \mathcal{D}(P_2)$

## Synchronous Parallel Composition

The **synchronous parallel composition** of systems  $\mathcal{D}_1$  and  $\mathcal{D}_2$ , denoted by  $\mathcal{D}_1 \parallel\!\!\! \parallel \mathcal{D}_2$ , is given by the FDS  $\mathcal{D} = \langle V, \Theta, \rho, \mathcal{J}, \mathcal{C} \rangle$ , where

$$\begin{aligned} V &= V_1 \cup V_2 \\ \Theta &= \Theta_1 \wedge \Theta_2 \\ \rho &= \rho_1 \wedge \rho_2 \\ \mathcal{J} &= \mathcal{J}_1 \cup \mathcal{J}_2 \\ \mathcal{C} &= \mathcal{C}_1 \cup \mathcal{C}_2 \end{aligned}$$

**Synchronous parallel composition** can be used for hardware verification, where it is the natural operator for combining two circuits into a composed circuit. Here we use it for **model checking** of **LTL** formulas.

**Claim 2.** The sequence  $\sigma$  of  $V$ -states is a computation of the combined  $\mathcal{D}_1 \parallel\!\!\! \parallel \mathcal{D}_2$  iff  $\sigma \downarrow_{V_1}$  is a computation of  $\mathcal{D}_1$  and  $\sigma \downarrow_{V_2}$  is a computation of  $\mathcal{D}_2$ .

Here,  $\sigma \downarrow_{V_i}$  denotes the sequence obtained from  $\sigma$  by restricting each of the states to a  $V_i$ -state.

## Feasibility and Viability of Systems

An FDS  $\mathcal{D}$  is said to be **feasible** if  $\mathcal{D}$  has at least one computation.

A finite or infinite sequence of states is defined to be a **run** of an FDS  $\mathcal{D}$  if it satisfies the requirements of **initiality** and **consecution** but not necessarily any of the **fairness** requirements.

The FDS  $\mathcal{D}$  is defined to be **viable** if any finite run of  $\mathcal{D}$  can be extended to a computation of  $\mathcal{D}$ .

**Claim 3.** Every FDS derived from an SPL program is viable.

Note that if  $\mathcal{D}$  is a viable system, such that its initial condition  $\Theta_{\mathcal{D}}$  is satisfiable, then  $\mathcal{D}$  is feasible.

## Requirement Specification Language: Temporal Logic

Assume an underlying (first-order) **assertion language**. The predicate  $\text{at}_{\ell_i}$ , abbreviates the formula  $\pi_j = \ell_i$ , where  $\ell_i$  is a location within process  $P_j$ .

A **temporal formula** is constructed out of state formulas (assertions) to which we apply the boolean operators  $\neg$  and  $\vee$  and the basic temporal operators:

$$\begin{aligned} \bigcirc & \text{ -- Next} & \ominus & \text{ -- Previous} \\ \mathcal{U} & \text{ -- Until} & \mathcal{S} & \text{ -- Since} \end{aligned}$$

Other temporal operators can be defined in terms of the basic ones as follows:

$$\begin{aligned} \blacklozenge p &= 1 \mathcal{U} p & \text{ -- Eventually} \\ \blacktriangleleft p &= \neg \blacklozenge \neg p & \text{ -- Henceforth} \\ p \mathcal{W} q &= \blacktriangleleft p \vee (p \mathcal{U} q) & \text{ -- Waiting-for, Unless, Weak Until} \\ \blacklozenge p &= 1 \mathcal{S} p & \text{ -- Sometimes in the past} \\ \blacktriangleleft p &= \neg \blacklozenge \neg p & \text{ -- Always in the past} \\ p \mathcal{B} q &= \blacktriangleleft p \vee (p \mathcal{S} q) & \text{ -- Back-to, Weak Since} \end{aligned}$$

A **model** for a temporal formula  $p$  is an infinite sequence of states  $\sigma : s_0, s_1, \dots$ , where each state  $s_j$  provides an interpretation for the variables of  $p$ .

## Semantics of LTL

Given a model  $\sigma$ , we define the notion of a temporal formula  $p$  holding at a position  $j \geq 0$  in  $\sigma$ , denoted by  $(\sigma, j) \models p$ :

- For an assertion  $p$ ,  
 $(\sigma, j) \models p \iff s_j \models p$   
 That is, we evaluate  $p$  locally on state  $s_j$ .
- $(\sigma, j) \models \neg p \iff (\sigma, j) \not\models p$
- $(\sigma, j) \models p \vee q \iff (\sigma, j) \models p \text{ or } (\sigma, j) \models q$
- $(\sigma, j) \models \bigcirc p \iff (\sigma, j+1) \models p$
- $(\sigma, j) \models p \mathcal{U} q \iff$  for some  $k \geq j$ ,  $(\sigma, k) \models q$ ,  
 and for every  $i$  such that  $j \leq i < k$ ,  $(\sigma, i) \models p$
- $(\sigma, j) \models \ominus p \iff j > 0$  and  $(\sigma, j-1) \models p$
- $(\sigma, j) \models p \mathcal{S} q \iff$  for some  $k \leq j$ ,  $(\sigma, k) \models q$ ,  
 and for every  $i$  such that  $j \geq i > k$ ,  $(\sigma, i) \models p$

This implies the following semantics for the derived operators:

- $(\sigma, j) \models \Box p \iff (\sigma, k) \models p$  for all  $k \geq j$
- $(\sigma, j) \models \Diamond p \iff (\sigma, k) \models p$  for some  $k \geq j$

If  $(\sigma, 0) \models p$  we say that  $p$  holds over  $\sigma$  and write  $\sigma \models p$ . Formula  $p$  is **satisfiable** if it holds over some model. Formula  $p$  is **(temporally) valid** if it holds over all models.

Formulas  $p$  and  $q$  are **equivalent**, denoted  $p \sim q$ , if  $p \leftrightarrow q$  is valid. They are called **congruent**, denoted  $p \approx q$ , if  $\Box(p \leftrightarrow q)$  is valid. If  $p \approx q$  then  $p$  can be replaced by  $q$  in any context.

The **entailment**  $p \Rightarrow q$  is an abbreviation for  $\Box(p \rightarrow q)$ .

For an FDS  $\mathcal{D}$  and an LTL formula  $\varphi$ , we say that  $\varphi$  is  **$\mathcal{D}$ -valid**, denoted  $\mathcal{D} \models \varphi$ , if all computations of  $\mathcal{D}$  satisfy  $\varphi$ .

## Reading Exercises

Following are some temporal formulas  $\varphi$  and a verbal formulation of the constraint they impose on a state sequence  $\sigma : s_0, s_1, \dots$  such that  $\sigma \models \varphi$ :

- $p \rightarrow \Diamond q$  — If  $p$  holds at  $s_0$ , then  $q$  holds at  $s_j$  for some  $j \geq 0$ .
- $\Box(p \rightarrow \Diamond q)$  — Every  $p$  is followed by a  $q$ . Can also be written as  $p \Rightarrow \Diamond q$ .
- $\Box \Diamond q$  — The sequence  $\sigma$  contains infinitely many  $q$ 's.
- $\Diamond \Box q$  — All but finitely many states in  $\sigma$  satisfy  $q$ . Property  $q$  eventually stabilizes.
- $q \Rightarrow \Diamond p$  — Every  $q$  is preceded by a  $p$  — **causality**.
- $(\neg r) \mathcal{W} q$  —  $q$  **precedes**  $r$ .  $r$  cannot occur before  $q$  — **precedence**. Note that  $q$  is not guaranteed, but  $r$  cannot happen without a preceding  $q$ .
- $(\neg r) \mathcal{W} (q \wedge \neg r)$  —  $q$  **strongly precedes**  $r$ .
- $p \Rightarrow (\neg r) \mathcal{W} q$  — Following every  $p$ ,  $q$  precedes  $r$ .

## Classification of Formulas/Properties

A formula of the form  $\Box p$  for some **past** formula  $p$  is called a **safety** formula.

A formula of the form  $\Box \Diamond p$  for some **past** formula  $p$  is called a **response** formula.

An equivalent characterization is the form  $p \Rightarrow \Diamond q$ . The equivalence is justified by

$$\Box(p \rightarrow \Diamond q) \sim \Box \Diamond ((\neg p) \mathcal{B} q)$$

Both formulas state that either there are infinitely many  $q$ 's, or there are no  $p$ 's, or there is a last  $q$ -position, beyond which there are no further  $p$ 's.

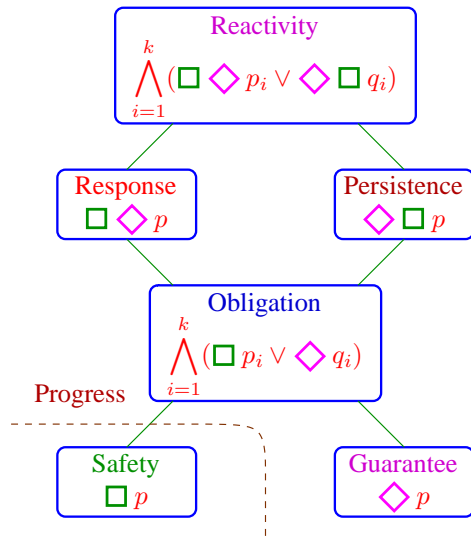
A property is classified as a **safety/response** property if it can be specified by a **safety/response** formula.

Every temporal formula is equivalent to a conjunction of a **reactivity** formulas, i.e.

$$\bigwedge_{i=1}^k (\Box \Diamond p_i \vee \Diamond \Box q_i)$$



## Hierarchy of the Temporal Properties



where  $p, p_i, q, q_i$  are past formulas.

## Temporal Specification of Properties

Formula  $\varphi$  is  $\mathcal{D}$ -valid, denoted  $\mathcal{D} \models \varphi$ , if all initial states of  $\mathcal{D}$  satisfy  $\varphi$ . Such a formula specifies a property of  $\mathcal{D}$ .

Following is a temporal specification of the main properties of program MUX-SEM.

$y$ : natural initially  $y = 1$

$$P_1 :: \left[ \begin{array}{l} l_0 : \text{loop forever do} \\ l_1 : \text{Non-critical} \\ l_2 : \text{request } y \\ l_3 : \text{Critical} \\ l_4 : \text{release } y \end{array} \right] \quad || \quad P_2 :: \left[ \begin{array}{l} m_0 : \text{loop forever do} \\ m_1 : \text{Non-critical} \\ m_2 : \text{request } y \\ m_3 : \text{Critical} \\ m_4 : \text{release } y \end{array} \right]$$

- **Mutual Exclusion** – No computation of the program can include a state in which process  $P_1$  is at  $l_3$  while  $P_2$  is at  $m_3$ . Specifiable by the formula

$$\Box \neg(\text{at}_{l_3} \wedge \text{at}_{m_3})$$

- **Accessibility for  $P_1$**  – Whenever process  $P_1$  is at  $l_2$ , it shall eventually reach its critical section at  $l_3$ . Specifiable by the formula

$$\Box (\text{at}_{l_2} \rightarrow \Diamond \text{at}_{l_3})$$

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## Model Checking

This is a process by which we algorithmically check that a given finite state FDS  $\mathcal{D}$  satisfies its temporal specification  $\varphi$ . There are two approaches to this process:

- **Enumerative (explicit state)** approach, by which we construct a graph containing all the reachable states of the system, and then apply graph theoretic algorithms to its analysis.
- **Symbolic** approach, by which we continuously work with assertions which characterize sets of states.

Here, we consider the symbolic approach. Note that every assertion over a finite-domain FDS can be represented as a boolean formula over boolean variables. Assume that a finite-state FDS is represented by such formulas, including the initial condition  $\Theta$  and the bi-assertion  $\rho$  representing the transition relation.

We assume that we have an efficient representation of boolean assertions, and efficient algorithms for manipulation of such assertions, including all the boolean operations as well as existential and universal quantification. Note that, for a boolean variable  $b$ ,

$$\exists b : \varphi(b) = \varphi(0) \vee \varphi(1) \quad \forall b : \varphi(b) = \varphi(0) \wedge \varphi(1)$$

Also assume that we can efficiently check whether a given assertion is valid, i.e., equivalent to 1.

## Successors and Their Transitive Closure

For an assertions  $\varphi(V)$  and a bi-assertion  $R(V, V')$ , we define the **existential successor predicate transformer**:

$$\varphi \diamond R = \text{unprime}(\exists V' : \varphi(V) \wedge R(V, V'))$$

Obviously

$$\|\varphi \diamond R\| = \{s \mid s \text{ is an } R\text{-successor of a } \varphi\text{-state}\}$$

For example

$$(x = 0) \diamond (x' = x + 1) = \text{unprime}(\exists x : x = 0 \wedge x' = x + 1) \sim \text{unprime}(x' = 1) \sim x = 1$$

The immediate successor transformer can be iterated to yield the **eventual successor transformer**:

$$\varphi \diamond R^* = \varphi \vee \varphi \diamond R \vee (\varphi \diamond R) \diamond R \vee ((\varphi \diamond R) \diamond R) \diamond R \vee \dots$$

## Predecessors and Their Transitive Closure

For an assertions  $\varphi(V)$  and a bi-assertion  $R(V, V')$ , we define the **existential predecessor predicate transformer**:

$$R \diamond \psi = \exists V' : R(V, V') \wedge \psi(V')$$

Obviously

$$\|R \diamond \varphi\| = \{s \mid s \text{ is an } R\text{-predecessor of a } \varphi\text{-state}\}$$

For example

$$(x' = x + 1) \diamond (x = 1) = \exists x' : x' = x + 1 \wedge x' = 1 \sim x = 0$$

The immediate predecessor transformer can be iterated to yield the **eventual predecessor transformer**:

$$R^* \diamond \varphi = \varphi \vee R \diamond \varphi \vee R \diamond (R \diamond \varphi) \vee R \diamond (R \diamond (R \diamond \varphi)) \vee \dots$$

## Formulation as Fixed Points

Consider a recursive equation of the general form  $y = f(y)$ , where  $y$  is an assertion representing a set of states. Such an equation is called a **fix-point equation**.

Not every fix-point equation has a solution. For example, the equation  $y = \neg y$  has no solution.

The assertional expression  $f(y)$  is called **monotonic** if it satisfies the requirement

$$\|y_1\| \subseteq \|y_2\| \text{ implies } \|f(y_1)\| \subseteq \|f(y_2)\|$$

## Solutions to Fix-point Equations

Every assertional expression  $f(y)$  which is constructed out of the assertion variable  $y$  and arbitrary constant assertions, to which we apply the **boolean operators**  $\vee$  and  $\wedge$ , and the **predecessor operator**  $\rho \diamond p$  is monotonic.

Consider a fix-point equation

$$y = f(y) \tag{1}$$

It may have 0, one, or many solutions. For example, the equation  $y = y$  has many solutions. A solution  $y_m$  is called a **minimal solution** if it satisfies  $\|y_m\| \subseteq \|y\|$  for any solution  $y$  of Equation (1). A solution  $y_M$  is called a **maximal solution** if it satisfies  $\|y_M\| \supseteq \|y\|$  for any solution  $y$  of Equation (1). We denote by  $\mu y.f(y)$  and  $\nu y.f(y)$  the minimal and maximal solutions, respectively.

**Claim 4.** *If  $f(y)$  is a monotonic expression, then the fix-point equation  $y = f(y)$  has both a minimal and a maximal solution which can be obtained by the iteration sequence*

$$y_1 = f(y_0), y_2 = f(y_1), y_3 = f(y_2), \dots$$

where  $y_0 = 0$  for the minimal solution, and  $y_0 = 1$  for the maximal solution.

## Expressing the Eventual Predecessor

A generalized version of the eventual predecessor can be expressed by a minimal fix-point expression:

$$(p \wedge \rho_{\mathcal{D}})^* \diamond q = \mu y. (q \vee p \wedge \rho_{\mathcal{D}} \diamond y)$$

This is because the fix-point expression generates the following approximation sequence:

$$\begin{aligned} y_0 &= 0 \\ y_1 &= q \vee 0 = q \\ y_2 &= q \vee p \wedge \rho_{\mathcal{D}} \diamond y_1 = q \vee p \wedge \rho_{\mathcal{D}} \diamond q \\ y_3 &= q \vee p \wedge \rho_{\mathcal{D}} \diamond y_2 = q \vee p \wedge \rho_{\mathcal{D}} \diamond q \vee p \wedge \rho_{\mathcal{D}} \diamond (p \wedge \rho_{\mathcal{D}} \diamond q) \\ &\quad \dots \end{aligned}$$

Characterizing the set of all states which initiate a  $p$ -path leading to a  $q$ -state.

## A Symbolic Algorithm for Model Checking Invariance

**Algorithm**  $\text{INV}(\mathcal{D}, p)$  : **assertion** — Check that FDS  $\mathcal{D}$  satisfies  $\text{Inv}(p)$ , using symbolic operations

```

new : assertion
1. new := ¬p
2. Fix (new) do
3.   new := new ∨ (ρℳ ◊ new)
4. return Θℳ ∧ new

```

where

**Fix** ( $y$ ) **do**  $S$  =  $old := \neg y$ ; **While** ( $y \neq old$ ) **do** [ $old := y$ ;  $S$ ]

The algorithm returns an assertion characterizing all the initial states from which there exists a finite path leading to violation of  $p$ . It returns the empty (**false**) assertion iff  $\mathcal{D}$  satisfies  $\text{Inv}(p)$ .

An equivalent formulation is

**return**  $\Theta_{\mathcal{D}} \wedge \mu y : \neg p \vee \rho_{\mathcal{D}} \diamond y$

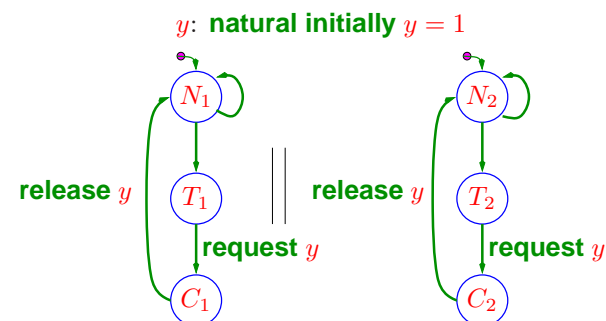
## Equivalent Iterations

There are several equivalent ways to compute the set of all eventual predecessors of an assertion  $\varphi$ :

$$\begin{aligned} \rho^* \diamond \varphi &\sim \\ \mu X. (\varphi \vee \rho \diamond X) &\sim \\ X := 0; \text{Fix}(X) [X := \varphi \vee \rho \diamond X] &\sim \\ X := \varphi; \text{Fix}(X) [X := X \vee \rho \diamond X] &\sim \\ \varphi \vee \rho \diamond \varphi \vee \rho \diamond (\rho \diamond \varphi) \vee \rho \diamond (\rho \diamond (\rho \diamond \varphi)) \vee \dots & \end{aligned}$$

## Example: a Simpler MUX-SEM

Below, we present a simpler version of program MUX-SEM.



The semaphore instructions **request**  $y$  and **release**  $y$  respectively stand for

$\langle \text{when } y = 1 \text{ do } y := 0 \rangle$  and  $y := 1$ .

### Illustrate on MUX-SEM

We iterate as follows:

$$\varphi_0 : \pi_1 = C \wedge \pi_2 = C$$

$$\varphi_1 : \varphi_0 \vee \left( \begin{array}{c} \dots \\ \vee \pi_1 = T \wedge y = 1 \wedge \pi'_1 = C \wedge y' = 0 \\ \vee \pi_2 = T \wedge y = 1 \wedge \pi'_2 = C \wedge y' = 0 \end{array} \right) \diamond (\pi_1 = \pi_2 = C)$$

~

$$\pi_1 = \pi_2 = C \vee \pi_1 = T \wedge \pi_2 = C \wedge y = 1 \vee \pi_1 = C \wedge \pi_2 = T \wedge y = 1$$

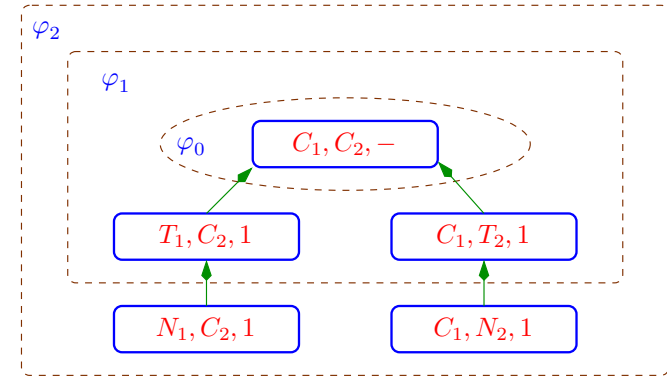
$$\varphi_2 : \varphi_1 \vee \pi_1 = N \wedge \pi_2 = C \wedge y = 1 \vee \pi_1 = C \wedge \pi_2 = N \wedge y = 1$$

$$\varphi_3 : \varphi_2 \vee \pi_1 = C \wedge \pi_2 = C \wedge y = 0 \sim \varphi_2$$

The last equivalence is due to the general property  $p \vee (p \wedge q) \sim p$ .

If we intersect  $\varphi_3$  with the initial condition  $\Theta : \pi_1 = N \wedge \pi_2 = N \wedge y = 1$  we obtain 0 (false). We conclude that MUX-SEM satisfies  $Inv(\neg(\pi_1 = C \wedge \pi_2 = C))$ .

### Symbolic Exploration Progresses in Layers

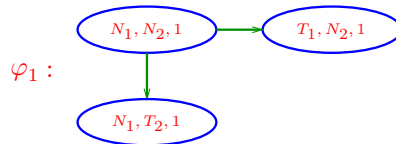


### Illustrate Forward Exploration on MUX-SEM

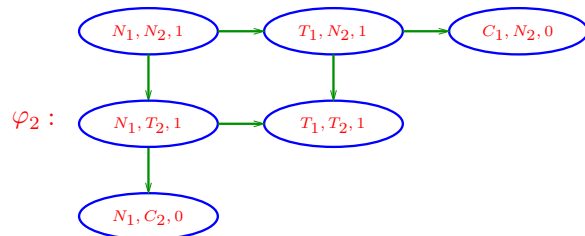
We iterate as follows:

$$\varphi_0 : N_1, N_2, 1$$

Iteration 1:

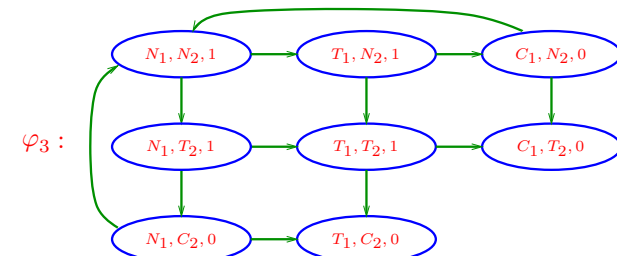


Iteration 2:

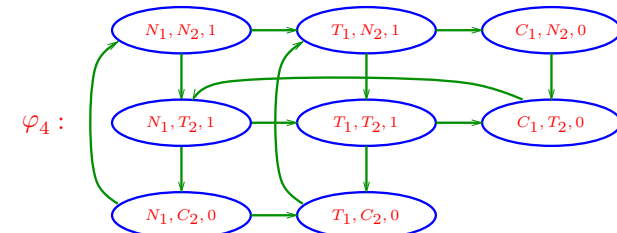


### Forward Exploration Continued

Iteration 3:



Iteration 4 (Convergent):



Since last iteration does not intersect  $C_1 \wedge C_2$ , we conclude  $\square \neg(C_1 \wedge C_2)$ .

## Checking for Feasibility

Before we discuss model checking **response properties** we discuss the problem of checking whether a given FDS is **feasible**.

A **run** of an FDS is an infinite sequence of states which satisfies the requirements of **initiality** and **consecution** but not necessarily any of the **fairness** requirements.

A state  $s$  of an FDS  $\mathcal{D}$  is called **reachable** if it participates in some run of  $\mathcal{D}$ .

A state  $s$  is called **feasible** if it participates in some computation. The FDS is called **feasible** if it has at least one computation.

A set of states  $S$  is defined to be an **F-set** if it satisfies the following requirements:

- F1. All states in  $S$  are reachable.
- F2. Each state  $s \in S$  has a  $\rho$ -successor in  $S$ .
- F3. For every state  $s \in S$  and every justice requirement  $J \in \mathcal{J}$ , there exists an  $S$ -path leading from  $s$  to some  $J$ -state.
- F4. For every state  $s \in S$  and every compassion requirement  $(p, q) \in \mathcal{C}$ , either there exists an  $S$ -path leading from  $s$  to some  $q$ -state, or  $s$  satisfies  $\neg p$ .

## F-Sets Imply Feasibility

### Claim 5. [F-sets]

A reachable state  $s$  is feasible iff it has a path leading to some F-set.

#### Proof:

Assume that  $s$  is a feasible state. Then it participates in some computation  $\sigma$ . Let  $S$  be the (finite) set of all states that appear infinitely many times in  $\sigma$ . We will show that  $S$  is an F-set. It is not difficult to see that there exists a cutoff position  $t \geq 0$  such that  $S$  contains all the states that appear at positions beyond  $t$ .

Obviously all states appearing in  $\sigma$  are reachable. If  $s \in S$  appears in  $\sigma$  at position  $i > t$  then it has a successor  $s_{i+1} \in \sigma$  which is also a member of  $S$ .

Let  $s = s_i \in \sigma, i > t$  be a member of  $S$  and  $J \in \mathcal{J}$  be some justice requirement. Since  $\sigma$  is a computation it contains infinitely many  $J$ -positions. Let  $k \geq i$  one of the  $J$ -positions appearing later than  $i$ . Then the path  $s_i, \dots, s_k$  is an  $S$ -path leading from  $s$  to a  $J$ -state.

Let  $s = s_i \in \sigma, i > t$  be a member of  $S$  and  $(p, q) \in \mathcal{C}$  be some compassion requirement. There are two possibilities by which  $\sigma$  may satisfy  $(p, q)$ . Either  $\sigma$  contains only finitely many  $p$ -positions, or  $\sigma$  contains infinitely many  $q$  positions. It follows that either  $S$  contains no  $p$ -states, or it contains some  $q$ -states which appear infinitely many times in  $\sigma$ . In the first case,  $s$  satisfies  $\neg p$ . In the second case, there exists a path leading from  $s_i$  to  $s_k$ , a  $q$ -state such that  $k \geq i$ .

## Proof Continued

In the other direction, assume the existence of an F-set  $S$  and a reachable state  $s$  which has a path leading to some state  $s_1 \in S$ . We will show that there exists a computation  $\sigma$  which contains  $s$ .

Since  $s$  is reachable and has a path leading to state  $s_1 \in S$ , there exists a finite sequence of states  $\pi$  leading from an initial state to  $s_1$  and passing through  $s$ . We will show how  $\pi$  can be extended to a computation by an infinite repetition of the following steps. At any point in the construction, we denote by  $\text{end}(\pi)$  the state which currently appears last in  $\pi$ .

- We know that  $\text{end}(\pi) \in S$  has a successor  $s \in S$ . Append  $s$  to the end of  $\pi$ .
- Consider in turn each of the justice requirements  $J \in \mathcal{J}$ . We append to  $\pi$  the  $S$ -path  $\pi_J$  connecting  $\text{end}(\pi)$  to a  $J$ -state.
- Consider in turn each of the compassion requirements  $(p, q) \in \mathcal{C}$ . If there exists an  $S$ -path  $\pi_q$ , connecting  $\text{end}(\pi)$  to a  $q$ -state, we append  $\pi_q$  to the end of  $\pi$ . Otherwise, we do not modify  $\pi$ . We observe that if there does not exist an  $S$ -path leading from  $\text{end}(\pi)$  to a  $q$ -state, then  $\text{end}(\pi)$  and all of its progeny within  $S$  must satisfy  $\neg p$ .

It is not difficult to see that the infinite sequence constructed in this way is a computation.  $\blacksquare$

## Computing F-Sets

Assume an assertion  $\varphi$  which characterizes an F-set. Translating the requirements 1–4 into formulas, we obtain the following requirements:

$$\begin{array}{ll} \varphi & \rightarrow \text{reachable}_{\mathcal{D}} \\ \varphi & \rightarrow \rho \diamond \varphi \quad \text{Every } \varphi\text{-state has a } \varphi\text{-successor} \\ \varphi & \rightarrow (\varphi \wedge \rho)^* \diamond (\varphi \wedge J) \quad \text{For every } J \in \mathcal{J} \\ \varphi & \rightarrow \neg p \vee (\varphi \wedge \rho)^* \diamond (\varphi \wedge q) \quad \text{For every } (p, q) \in \mathcal{C} \end{array}$$

This can be summarized as

$$\varphi \rightarrow \left[ \begin{array}{c} \text{reachable}_{\mathcal{D}} \\ \bigwedge_{J \in \mathcal{J}} (\varphi \wedge \rho)^* \diamond (\varphi \wedge J) \end{array} \wedge \begin{array}{c} \rho \diamond \varphi \\ \bigwedge_{(p, q) \in \mathcal{C}} \neg p \vee (\varphi \wedge \rho)^* \diamond (\varphi \wedge q) \end{array} \right]$$

Since we are interested in a maximal F-set, the computation can be expressed as:

$$\nu \varphi. \left[ \begin{array}{c} \text{reachable}_{\mathcal{D}} \\ \bigwedge_{J \in \mathcal{J}} (\varphi \wedge \rho)^* \diamond (\varphi \wedge J) \end{array} \wedge \begin{array}{c} \rho \diamond \varphi \\ \bigwedge_{(p, q) \in \mathcal{C}} \neg p \vee (\varphi \wedge \rho)^* \diamond (\varphi \wedge q) \end{array} \right]$$

## Algorithmic Interpretation

Computing the maximal fix-point as a sequence of iterations, we can describe the computational process as follows:

Start by letting  $\varphi := \text{reachable}_D$ . Then repeat the following steps:

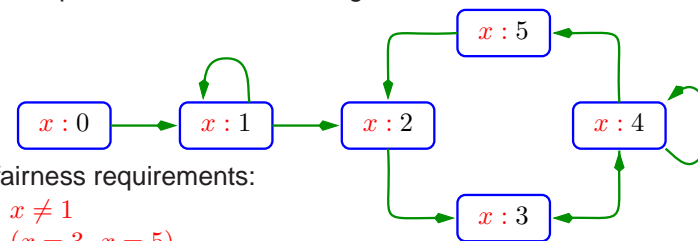
- Remove from  $\varphi$  all states which do not have a  $\varphi$ -successor.
- For each  $J \in \mathcal{J}$ , remove from  $\varphi$  all states which do not have a  $\varphi$ -path leading to a  $J$ -state.
- For each  $(p, q) \in \mathcal{C}$ , remove from  $\varphi$  all  $p$ -states which do not have a  $\varphi$ -path leading to a  $q$ -state.

until no further change.

To check whether an FDS  $\mathcal{D}$  is feasible, we compute for it the maximal **F**-set and check whether it is empty.  $\mathcal{D}$  is feasible iff the maximal **F**-set is not-empty.

## Example

As an example, consider the following FDS:



with the fairness requirements:

$$\begin{aligned} J_1 &: x \neq 1 \\ C_1 &: (x = 3, x = 5) \\ C_2 &: (x = 2, x = 1) \end{aligned}$$

We set  $\varphi_0 : \{0..5\}$  and then proceed as follows:

- Removing from  $\varphi_0$  all  $(x = 2)$ -states which do not have a  $\varphi_0$ -path leading to an  $(x = 1)$ -state, we are left with  $\varphi_1 : \{0, 1, 3, 4, 5\}$ .
- Successively removing from  $\varphi_1$  all states without successors, leaves  $\varphi_2 : \{3, 4\}$ .
- Removing from  $\varphi_2$  all  $(x = 3)$ -states which do not have a  $\varphi_2$ -path leading to a  $(x = 5)$ -state, we are left with  $\varphi_3 : \{4\}$ .
- No reasons to remove any further states from  $\varphi_3 : \{4\}$ , so this is our final set.

We conclude that the above FDS is **feasible**.

## Verifying Response Properties Through Feasibility Checking

Let  $\mathcal{D} : \langle V, \Theta, \rho, \mathcal{J}, \mathcal{C} \rangle$  be an FDS and  $p \Rightarrow \diamond q$  be a response property we wish to verify over  $\mathcal{D}$ . Let  $\text{reachable}_D$  be the assertion characterizing all the reachable states in  $\mathcal{D}$ .

We define an auxiliary FDS  $\mathcal{D}_{p,q} : \langle V, \Theta_{p,q}, \rho_{p,q}, \mathcal{J}, \mathcal{C} \rangle$ , where

$$\begin{aligned} \Theta_{p,q} &: \text{reachable}_D \wedge p \wedge \neg q \\ \rho_{p,q} &: \rho \wedge \neg q' \end{aligned}$$

Thus,  $\Theta_{p,q}$  characterizes all the  $\mathcal{D}$ -reachable  $p$ -states which do not satisfy  $q$ , while  $\rho_{p,q}$  allows any  $\rho$ -step as long as the successor does not satisfy  $q$ .

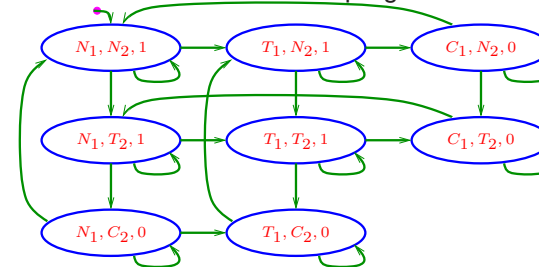
### Claim 6. [Model Checking Response]

$\mathcal{D} \models p \Rightarrow \diamond q$  iff  $\mathcal{D}_{p,q}$  is **unfeasible**.

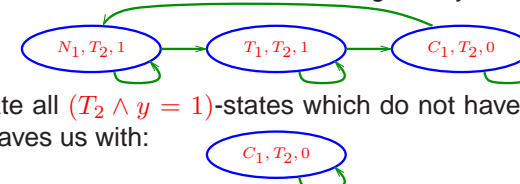
**Proof:** The claim is justified by the observation that every computation of  $\mathcal{D}_{p,q}$  can be extendable to a computation of  $\mathcal{D}$  which violates the response property  $p \Rightarrow \diamond q$ . Indeed, let  $\sigma : s_k, s_{k+1}, \dots$  be a computation of  $\mathcal{D}_{p,q}$ . By the definition of  $\Theta_{p,q}$ , we know that  $s_k$  is a  $\mathcal{D}$ -reachable  $p$ -state. Thus, there exists, a finite sequence  $s_0, \dots, s_k$ , such that  $s_0$  is  $\mathcal{D}$ -initial. The infinite sequence  $s_0, \dots, s_{k-1}, s_k, s_{k+1}, \dots$  is a computation of  $\mathcal{D}$  which contains a  $p$ -state at position  $k$ , and has no following  $q$ -state. This sequence violates  $p \Rightarrow \diamond q$ .  $\blacksquare$

## Example: MUX-SEM

Following is the set of all reachable states of program MUX-SEM.



Assume we wish to verify the property  $T_2 \Rightarrow \diamond C_2$ . We start by forming  $\text{MUX-SEM}_{T_2, C_2}$ , whose set of reachable states is given by:



First, we eliminate all  $(T_2 \wedge y = 1)$ -states which do not have a path leading to a  $C_2$ -state. This leaves us with:



Next, we eliminate all states which do not have a path leading to a  $\neg C_1$ -state. This leaves us with nothing. We conclude that  $\text{MUX-SEM} \models T_2 \Rightarrow \diamond C_2$ .

## Lectures Outline

- Overview of System Synthesis.
- Fair Discrete Systems and their Computations.
- Model Checking **Invariance** and **response**.
- **Temporal Testers** and general **LTL** Model Checking.
- **Controller Synthesis** via **Games**.
- Synthesis from **Recurrence** Specifications.
- Synthesis from **Reactivity** Specifications. – The general case.

## Model Checking General Temporal Formulas

Next, we consider methods for model checking general **LTL** formulas.

Let  $\mathcal{D}$  be an **FDS** and  $\varphi$  an **LTL** formula. Assume we wish to check whether  $\mathcal{D} \models \varphi$ . We proceed along the following steps:

- Construct a **temporal acceptor**  $\mathcal{A}(\neg\varphi)$ . This is an **FDS** whose computations are all the sequences falsifying  $\varphi$ .
- Form the parallel composition  $\mathcal{D} \parallel \mathcal{A}(\neg\varphi)$ . This is an **FDS** whose computations are all computations of  $\mathcal{D}$  which violate  $\varphi$ .
- Check whether the composition  $\mathcal{D} \parallel \mathcal{A}(\neg\varphi)$  is **feasible**.  $\mathcal{D} \models \varphi$  iff  $\mathcal{D} \parallel \mathcal{A}(\neg\varphi)$  is **infeasible**.

It only remains to describe the construction of an **acceptor**  $\mathcal{A}(\psi)$  for a general **LTL** formula  $\psi$ .

## Temporal Testers

The building blocks from which we construct acceptors are temporal testers. Let  $\varphi$  be a temporal formula over vocabulary  $U$ , and let  $x \notin U$  be a boolean variable disjoint from  $U$ .

In the following, let  $\sigma : s_0, s_1, \dots$  be an infinite sequence of states over  $U \cup \{x\}$ . We say that  $x$  **matches**  $\varphi$  in  $\sigma$  if, for every position  $j \geq 0$ , the value of  $x$  at position  $j$  is true iff  $(\sigma, j) \models \varphi$ .

A **temporal tester** for  $\varphi$  is an **FDS**  $T(\varphi)$  over  $U \cup \{x\}$ , satisfying the requirement:

The infinite sequence  $\sigma$  is a computation of  $T(\varphi)$  iff  $x$  matches  $\varphi$  in  $\sigma$ .

A consequence of this definition is that every infinite sequence  $\pi$  of  $U$ -states can be extended into a computation  $\sigma$  of  $T(\varphi)$  by interpreting  $x$  at position  $j \geq 0$  of  $\sigma$  as 1 iff  $(\pi, j) \models \varphi$ .

We can view  $T(\varphi)$  as a (possibly non-deterministic) **transducer** which incrementally reads the values of the variables  $U$  and outputs in  $x$  the current value of  $\varphi$  over the infinite sequence.

## Construction of Temporal Testers

A formula  $\varphi$  is called a **principally temporal formula** (**PTF**) if the main operator of  $\varphi$  is temporal. A **PTF** is called a **basic temporal formula** if it contains no other **PTF** as a proper sub-formula.

We start our construction by presenting temporal testers for the basic temporal formulas.

## A Tester for $\bigcirc p$

The tester for the formula  $\bigcirc p$  is given by:

$$T(\bigcirc p) : \begin{cases} V : \text{Vars}(p) \cup \{x\} \\ \Theta : 1 \\ \rho : x = p' \\ \mathcal{J} = \mathcal{C} : \emptyset \end{cases}$$

### Claim 7.

$T(\bigcirc p)$  is a temporal tester for  $\bigcirc p$ .

### Proof:

Let  $\sigma$  be a computation of  $T(\bigcirc p)$ . We will show that  $x$  matches  $\bigcirc p$  in  $\sigma$ . Let  $j \geq 0$  be any position. By the transition relation,  $x = 1$  at position  $j$  iff  $s_{j+1} \models p$  iff  $(\sigma, j) \models \bigcirc p$ .

Let  $\sigma$  be an infinite sequence such that  $x$  matches  $\bigcirc p$  in  $\sigma$ . We will show that  $\sigma$  is a computation of  $T(\bigcirc p)$ . For any position  $j \geq 0$ ,  $x = 1$  at  $j$  iff  $(\sigma, j) \models \bigcirc p$ , iff  $s_{j+1} \models p$ . Thus,  $x$  satisfies  $x = p'$  at every position  $j$ .  $\blacksquare$

## A Tester for $p\mathcal{U}q$

The tester for the formula  $p\mathcal{U}q$  is given by:

$$T(p\mathcal{U}q) : \begin{cases} V : \text{Vars}(p, q) \cup \{x\} \\ \Theta : 1 \\ \rho : x = q \vee (p \wedge x') \\ \mathcal{J} : q \vee \neg x \\ \mathcal{C} : \emptyset \end{cases}$$

**Claim 8.**  $T(p\mathcal{U}q)$  is a temporal tester for  $p\mathcal{U}q$ .

### Proof:

Based on the expansion formula

$$p\mathcal{U}q = q \vee (p \wedge \bigcirc(p\mathcal{U}q))$$

## Why Do We Need the Justice Requirement

Reconsider the temporal tester for  $p\mathcal{U}q$ :

$$T(p\mathcal{U}q) : \begin{cases} V : \text{Vars}(p, q) \cup \{x\} \\ \Theta : 1 \\ \rho : x = q \vee (p \wedge x') \\ \mathcal{J} : q \vee \neg x \\ \mathcal{C} : \emptyset \end{cases}$$

We wish to show that the justice requirement  $q \vee \neg x$  is essential for the correctness of the construction. Consider a state sequence  $\sigma : s_0, s_1, \dots$  in which  $q$  is identically false and  $p$  is identically true at all positions. Obviously,  $(\sigma, j) \not\models p\mathcal{U}q$ , for all  $j \geq 0$ , and the transition relation reduces to the equation

$$x = x'.$$

This equation has two possible solutions, one in which  $x$  is identically false and the other in which  $x$  is identically true at all positions. Only  $x = 0$  matches  $p\mathcal{U}q$ . This is also the only solution which satisfies the justice requirement.

Thus, the role of the justice requirement is to select among several solutions to the transition relation equation, a unique one which matches the basic temporal formula at all positions.

## A Tester for $p\mathcal{W}q$

A supporting evidence for the significance of the justice requirements is provided by the tester for the formula  $p\mathcal{W}q$ :

$$T(p\mathcal{W}q) : \begin{cases} V : \text{Vars}(p, q) \cup \{x\} \\ \Theta : 1 \\ \rho : x = q \vee (p \wedge x') \\ \mathcal{J} : \neg p \vee x \\ \mathcal{C} : \emptyset \end{cases}$$

Note that the transition relation of  $T(p\mathcal{W}q)$  is identical to that of  $T(p\mathcal{U}q)$ , and they only differ in their respective justice requirements.

The role of the justice requirement in  $T(p\mathcal{W}q)$  is to eliminate the solution  $x = 0$  over a computation in which  $p = 1$  and  $q = 0$  at all positions.



## Testers for the Derived Operators

Based on the testers for  $\mathcal{U}$  and  $\mathcal{W}$ , we can construct testers for the derived operators  $\diamond$  and  $\square$ . They are given by

$$T(\diamond p) : \begin{cases} V : \text{Vars}(p) \cup \{x\} \\ \Theta : 1 \\ \rho : x = p \vee x' \\ \mathcal{J} : p \vee \neg x \\ \mathcal{C} : \emptyset \end{cases} \quad T(\square p) : \begin{cases} V : \text{Vars}(p) \cup \{x\} \\ \Theta : 1 \\ \rho : x = p \wedge x' \\ \mathcal{J} : \neg p \vee x \\ \mathcal{C} : \emptyset \end{cases}$$

A formula such as  $\diamond p$  can be viewed as a “promise for an eventual  $p$ ”. The justice requirement  $p \vee \neg x$  can be interpreted as suggesting:

Either fulfill all your promises or stop promising.

Note that once  $x = 0$  in the tester  $T(\diamond p)$ , it remains 0 and requires  $p = 0$  ever after.

## Testers for the Basic Past Formulas

The following are testers for the basic past formulas  $\ominus p$  and  $pSq$ :

$$T(\ominus p) : \begin{cases} V : \text{Vars}(p) \cup \{x\} \\ \Theta : x = 0 \\ \rho : x' = p \\ \mathcal{J} : \emptyset \\ \mathcal{C} : \emptyset \end{cases} \quad T(pSq) : \begin{cases} V : \text{Vars}(p, q) \cup \{x\} \\ \Theta : x = q \\ \rho : x' = q' \vee (p' \wedge x) \\ \mathcal{J} : \emptyset \\ \mathcal{C} : \emptyset \end{cases}$$

Note that testers for past formulas are not associated with any fairness requirements. On the other hand, they have a non-trivial initial conditions.

## Testers for Compound Temporal Formulas

Up to now we only considered testers for basic formulas. The construction for non-basic formulas is based on the following reduction principle. Let  $f(\varphi)$  be a temporal formula containing one or more occurrences of the basic formula  $\varphi$ . Then the temporal tester for  $f(\varphi)$  can be constructed according to the following recipe:

$$T(f(\varphi)) = T(f(x_\varphi)) \parallel T(\varphi)$$

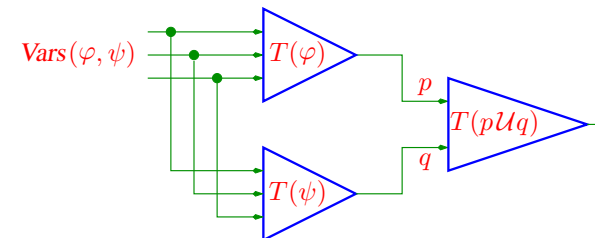
where,  $x_\varphi$  is the boolean output variable of  $T(\varphi)$ , and  $f(x_\varphi)$  is obtained from  $f(\varphi)$  by replacing every instance of  $\varphi$  by  $x_\varphi$ .

Following this recipe the temporal tester for an arbitrary formula  $f$  can be decomposed into a synchronous parallel composition of smaller testers, one for each basic formula nested within  $f$ .

When all possible substitution/composition steps are performed, we are left with a (non-temporal) assertion. We refer to this assertion as the **redux** of the original formula  $f$ , and denote it by  $\text{redux}(f)$ .

## Testers as Circuits

Having viewed testers as **transducers**, we can view their composition as a **circuit interconnection**. For example, in the following diagram we show how a tester for the compound formula  $\varphi \mathcal{U} \psi$  can be constructed by interconnecting the testers for  $\varphi$ ,  $\psi$ , and the tester for the basic formula  $p \mathcal{U} q$ .



## Acceptors

Testers are the essential building blocks for the construction of an **acceptor**. An acceptor for an LTL formula  $\varphi$  (over variables  $U$ ) is an FDS  $\mathcal{A}(\varphi)$  such that

The  $U$ -sequence  $\sigma$  satisfies  $\varphi$  iff  $\sigma$  is a  $U$ -projection of a computation of  $\mathcal{A}(\varphi)$ .

Thus, unlike testers, an acceptor only accepts at position 0.

The construction of an acceptor is defined recursively as follows:

- For an assertion  $p$ ,

$$\mathcal{A}(p) : \begin{cases} V : & \text{Vars}(p) \\ \Theta : & p \\ \rho : & 1 \\ \mathcal{J} = \mathcal{C} : & \emptyset \end{cases}$$

- For a formula  $f(\varphi)$  containing one or more occurrences of the basic formula  $\varphi$ ,

$$\mathcal{A}(f(\varphi)) = \mathcal{A}(f(x_\varphi)) \parallel T(\varphi)$$

## Example: An Acceptor for $\neg\Diamond\Box p$

Following is a tester for the formula  $\neg\Diamond\Box p$  which is obtained by computing the parallel composition  $\mathcal{A}(\neg x_\Diamond) \parallel T(\Diamond x_\Box) \parallel T(\Box p)$ .

$$\mathcal{A}(\Diamond\Box p) : \begin{cases} V : & \text{Vars}(p) \cup \{x_\Diamond, x_\Box\} \\ \Theta : & \neg x_\Diamond \\ \rho : & (x_\Box = p \wedge x_\Box') \wedge (x_\Diamond = x_\Box \vee x_\Diamond') \\ \mathcal{J} : & \{-p \vee x_\Box, x_\Box \vee \neg x_\Diamond\} \\ \mathcal{C} : & \emptyset \end{cases}$$

Note that the redux of  $\neg\Diamond\Box p$  is  $\neg x_\Diamond$ .

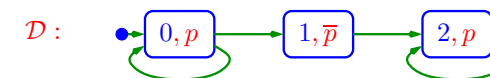
## Model Checking General Temporal Formulas

To check whether  $\mathcal{D} \models \varphi$ , perform the following steps:

- Construct** the acceptor  $\mathcal{A}(\neg\varphi)$ .
- Form** the combined system  $C = \mathcal{D} \parallel \mathcal{A}(\neg\varphi)$ .
- Check** whether  $C$  is feasible.
- Conclude**  $\mathcal{D} \models \varphi$  iff  $C$  is infeasible.

## Example

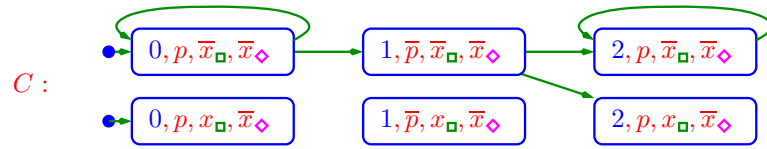
Consider the following system:



For which we wish to verify the property  $\Diamond\Box p$ .

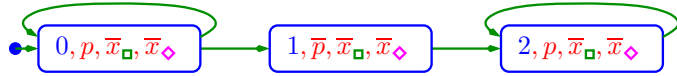
## Example: Continued

Composing the system with the acceptor  $\mathcal{A}(\neg \diamond \square p)$ , we obtain:



with the justice requirements  $\neg p \vee x_{\square}$  and  $x_{\square} \vee \neg x_{\diamond}$ .

Eliminating all unreachable states and states with no successors, we are left with:



State 2 is eliminated because it does not have a path leading to a  $\neg p \vee x_{\square}$ -state. Then state 1 is eliminated, having no successors. Finally, 0 is eliminated because it cannot reach a  $\neg p \vee x_{\square}$ -state. Nothing is left, hence the system satisfies the property  $\diamond \square p$ .

## Correctness of the Algorithms

### Claim 9.

For an FDS  $\mathcal{D}$  and temporal formula  $\varphi$ ,  $\mathcal{D} \models \varphi$  iff  $C : \mathcal{D} \parallel \mathcal{A}(\neg \varphi)$  is infeasible

### Proof:

The proof is based on the observation that every computation of the combined system  $C$  is a computation of  $\mathcal{D}$  which satisfies the negation of  $\varphi$ . Therefore, the existence of such a computation shows that not all computations of  $\mathcal{D}$  satisfy  $\varphi$ , and therefore,  $\varphi$  is not valid over  $\mathcal{D}$ .  $\blacksquare$